



Exploring Integrity of AEADs with Faults: Definitions and Constructions

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1. Fault Attacks (FA)
2. Attacks on Classical AEAD Schemes without Key Recovery
3. Levelled Implementations
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5. Fault Resilient MAC
6. Fault Resilient AEAD

Fault Attacks (FA)

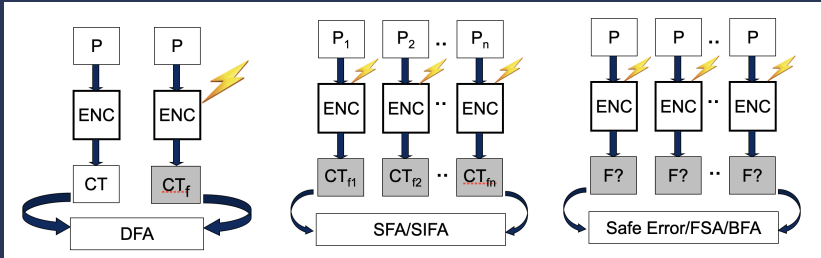
FAULT ATTACKS (FA)

- Fault Attacks (FA) have been introduced in 1997 [BDL97, BS97].
- Over the years, both analysis and fault injection techniques have improved significantly [TMA11, FJLT13, SBHS15, SH07, SBR⁺20, DEK⁺18, PCNM15, ZLZ⁺18, MOG⁺20, DEK⁺18, DEG⁺18, SBR⁺20, SBJ⁺21].
- Most fault attacks and fault countermeasures in symmetric key cryptography target key/state recovery.

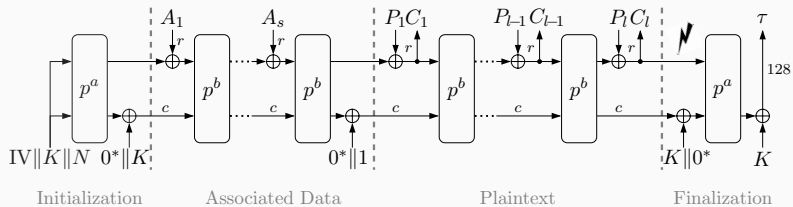
FAULT-RESILIENCY

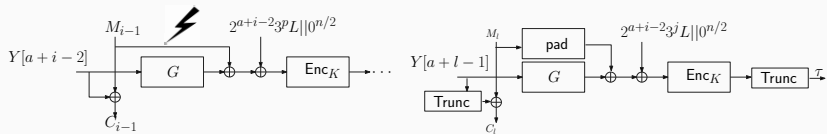
- Dobraunig, Mennink and Primas. [DMP20] discussed the security of sponge-like constructions where the amount of information leaked using faults is limited.
- Some papers discuss building primitives that protect against certain types of fault attacks [MSGR10, SBD⁺20, BBB⁺21].
- Fischlin and Günther [FG20] discussed the concept of fault-resilient AE and gave one construction.
- Saha, Khairallah and Peyrin (this work) discussed the definitions of the fault model and how to define different fault-resilient primitives to be able to use in AE scheme. We also show that the construction from [FG20] does not achieve frAE.
- In parallel to this work, Berti, Guo, Peters, Shen and Standaert [BGP⁺22] showed that it is possible to have frMAC with resiliency against verification faults. The final construction in their paper can be seen as an instantiation of our frMAC.

EXAMPLES OF FAULT ATTACKS



Attacks on Classical AEAD Schemes without Key Recovery



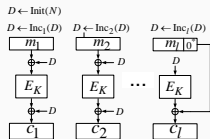


APPLIES TO MORE SCHEMES

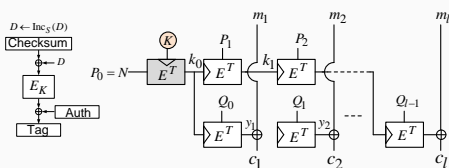
- SIV.
- Enc-then-MAC.

Levelled Implementations

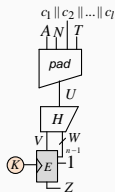
OCB vs. TEDT: PROTECTING LONG TERM SECRETS AT A CHEAPER COST



(a)

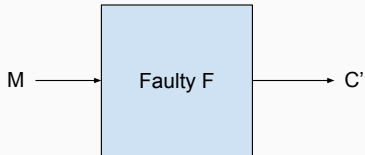
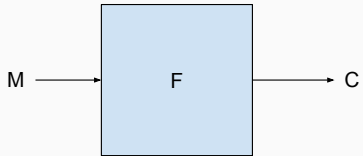


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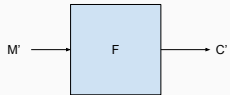
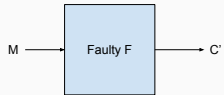
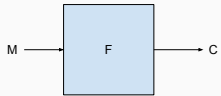


Fault Resilient PRF

WHAT HAPPENS WHEN WE INJECT FAULT?



WHAT HAPPENS WHEN WE INJECTS FAULT?



RESISTANCE VS. RESILIENCE

- We allow more trivial forgeries/distinguishers than allowed in a classical security notion.
- We allow a phase of the attack where we do not claim security for any message in that phase.

ADVERSARIAL SET UP

1. Training phase: the attacker gets description of the implementation with the ability to inject faults anywhere, but no direct access to the secret key. In this phase the implementation is always real.

ADVERSARIAL SET UP

1. Training phase: the attacker gets description of the implementation with the ability to inject faults anywhere, but no direct access to the secret key. In this phase the implementation is always real.
2. Attack phase: the attacker cannot inject faults any more. In this phase the oracle can be real or ideal.

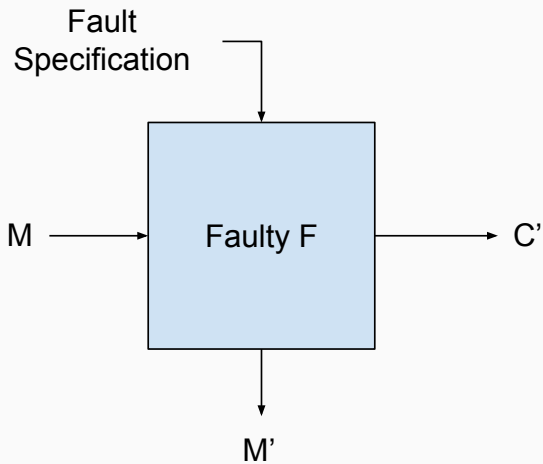
FAULT RESILIENT PRF GAME

Challenge: Faulty queries may (in theory) leak information about more than one evaluation of the function at a time.

frPRF

Real World	Ideal World
$\text{PRF}_K^f(M, \mathcal{F})$... faulty implementation. ...	$\text{PRF}_K^f(M, \mathcal{F})$... faulty implementation, but terminates if a faulty query leaks more than one point of the function. ...
$\text{PRF}_K(M)$... Real implementation with fresh inputs. ...	$\text{RF}_K(M)$... Random function with fresh inputs. ...

FAULTY PRF ORACLE

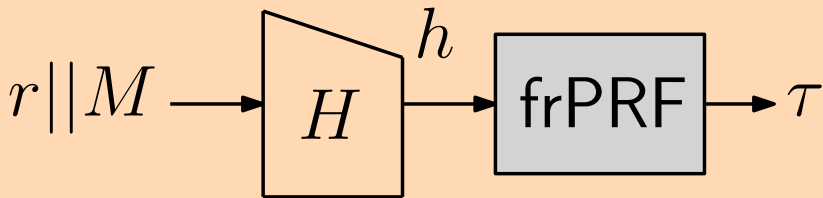


HOW TO BUILD AN FRPRF

1. We can construct such primitives using a tweakable block cipher protected against fault-attacks.
2. If the cipher does not allow key recovery through fault attacks, it should be possible to use as an frPRF.
3. It may be possible to show that ISAP finalization is an frPRF.
4. In practice, we may not know if the preimage is easy or not, but what this model says is that a small amount of trivial forgeries/distinguishers using faults is unavoidable.

Fault Resilient MAC

FRMAC



SECURITY ARGUMENTS

- No collision on the random salt, or the output of the hash (frRO).
- Only trivial preimages are possible.
- frMAC has security similar to frPRF, only need to worry about tag verification.

Fault Resilient AEAD

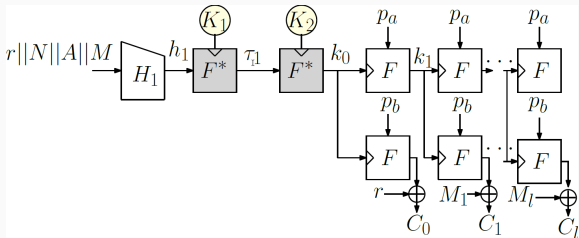
THE FRAE GAME

- Similar to the frPRF game but taking privacy and decryption into account.
- A variation of the game proposed in [FG20].

SIV\$	
KGen: 1: $(K_1, K_2) \xleftarrow{\$} \mathcal{K}$ 2: return (K_1, K_2)	$\text{Dec}_K^{\text{SIV\$}}(N, A, C')$: 1: $(K_1, K_2) \leftarrow K$ 2: $(IV, C) \leftarrow C'$ 3: $r M \leftarrow \text{Dec}(K_2, C, IV)$ 4: $IV' \leftarrow \text{PRF}(K_1, N, A, M, r)$ 5: if $IV = IV'$ 6: return M 7: else : 8: return \perp
$\text{Enc}_K^{\text{SIV\$}}(N, A, M; r)$: 1: $(K_1, K_2) \leftarrow K$ 2: $IV \leftarrow \text{PRF}(K_1, N, A, M; r)$ 3: $C \leftarrow \text{Enc}(K_2, r M, IV)$ 4: return (IV, C)	

DECOUPLING ATTACK ON ALL SIV-LIKE SCHEMES

1. Fault the MAC to make it give a tag for M' .
2. Encrypt M using the IV corresponding to M' .
3. (N, A, C, IV) is not a valid ciphertext.
4. C can be changed to C' corresponding to M' .

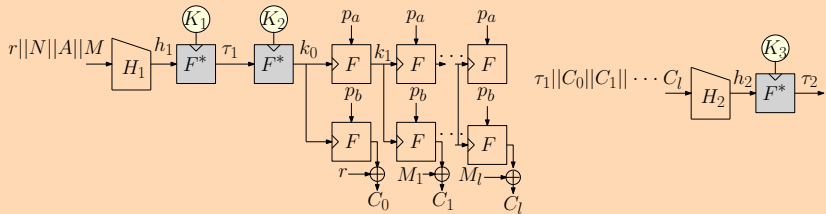


IF MAC-THEN-ENC DOES NOT WORK, WHAT DOES?

MAC-then-Enc

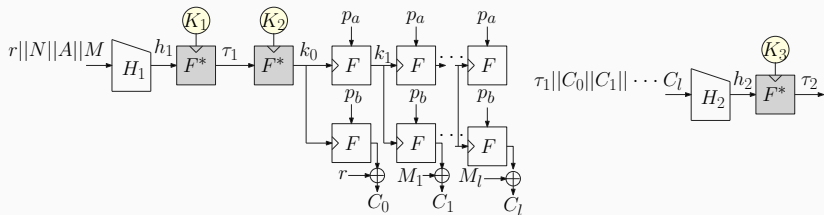
then MAC Again

MAC-ENC-MAC (MEM)



SECURITY ARGUMENTS

- No collision on the random salt, or the output of the first MAC.
- The security then reduces to the frMAC security of the two MACs and the frPRF security of the key derivation function in the encryption layer.



CONCLUSION

- It is possible to protect certain classes of fault attacks using levelled implementations.
- Randomness is critical to prevent differential fault attacks in unprotected primitives.
- It is possible to prevent single differential fault attacks with less cost and more effectively than dummy duplication.

FUTURE WORK

- Indifferentiability of randomized hash functions from frRO.
- Show frAE is secure against combined attacks (combined fault and leakage resilience).
- Protecting against multiple faults.
 - A solution to prevent a d-fault version of the decoupling attack may be to keep interleaving Enc and MAC (MEMEM...).
 - Is there a solution a solution that protects against arbitrary number of faults?
- Are there efficient solutions for the security of MAC against differential faults without randomness?
 - In parallel work, Bertini *et al.* [BGP⁺22] showed an example of a MAC that does not need randomness and protects against a single differential fault. It requires two MAC invocations.
 - A more efficient solution would need less than $i + 1$ invocations to protect against i differential faults.
- Relate the security of different fault countermeasures to the frPRF assumption (e.g. is ISAP's PRF an frPRF?).

Thank you!

More details in eprint 2022/1055



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Modelling Faults

- A classification is important.
- Physical faults eventually cause some data or control corruption.
- In general, localized corruptions are observed which can be one/multiple-bit flip or set/reset. For software implementations, we also observe instruction modification/skip.
- The precision of faults are dependent on the target device and injection instruments.

ABSTRACT MODEL OF PHYSICAL EVENTS

Fault Representation	
Params	Description
v_i	Denote the variables corrupted by faults. $v_i \in \text{data} \cup \text{control} \cup \text{constant}$.
nf	The number of faults injected throughout the computation (in the same or different clock cycles).
w_i	Denote the width (how many bits within a target variable are corrupted) of a fault ($0 \leq w_i \leq v_i $).
mod_i	The logical abstraction of physical nature of faults (fault models). $mod_i \in \text{fix} \cup \text{diff} \cup \text{rand} \cup \text{nof}$.
t_i	Denote if the fault is transient/persistent and the temporal fault location.

Variable Classification	
Params	Description
data	Denotes the set of data-flow variables (input, output and intermediate states of the computation).
control	Denotes the set of control-flow variables (branch statements).
constant	Denotes the set of constants, tables, and domain separators of the AEAD algorithm.

Fault Models	
fix	Denote faults where the adversary is allowed to fix w_i bits of the target variable to some desired value.
diff	Denote the differential faults where the adversary is allowed to select a bitwise differential Δ_i for variable v_i (with $HW(\Delta_i) = w_i$) and set $v_i' = v_i \oplus \Delta_i$. Here v_i' is the faulty version of v_i and $HW(\cdot)$ denote the Hamming weight.
rand	Same as diff except the fact that $\Delta_i \xleftarrow{\mathbb{S}} \{0, 1\}^{ v_i }$ and $HW(\Delta_i) = w_i$.
nof	Denotes the case when the adversary chooses not to inject a fault in the execution.

COMPARISON TO AN AEAD COMBINER [PR20]

- A different approach to prevent such failures would have been to use an AEAD combiner with two AEAD schemes.
- Assuming both schemes are based on SIV\$, this solution needs two MACs and two Encryption schemes.
- In fact, it is impossible to get an AEAD combiner with less than $4A + 4M$ for the encryption and decryption cost ([PR20]).
- The cheapest blackbox combiner for two SIV\$ schemes would need $2A + 6M$ for either encryption or decryption¹.
- Our solution only needs $A + 3M$.
- For ciphertext lengths, our scheme is on-par with AEAD combiners $M + 3\tau$.

¹[PR20] reports $2A + 3M$, but this is not considering that the AEAD scheme itself processes the message twice

THE FRAE GAME: TRAINING²

```
1: if  $d = 1$ 
2:   return  $\perp$ 
3:  $r \xleftarrow{\$} \mathcal{R}$ 
4:  $(\mathcal{N}_{flt}, \mathcal{AD}_{flt}, C) \leftarrow \text{Fault}(\text{MEMEnc}_K(N, A, M; r), \mathcal{F})$ 
5:  $\mathcal{I}_{vld} \leftarrow \{(N', A') \in \mathcal{N}_{flt} \times \mathcal{AD}_{flt} \mid$ 
    $\text{MEMDec}_K(N', A', C) \neq \perp \wedge (N', A', C) \notin S_{flt}\}$ 
6: if  $b = 1 \wedge |\mathcal{I}_{vld}| > 1$ 
7:   return  $\perp$ 
8:  $S_{flt} \leftarrow S_{flt} \cup \{(N', A', C) \mid$ 
    $(N', A') \in \mathcal{I}_{vld}\}$ 
9: return  $C$ 
```

A variation of the game proposed in [FG20]

THE FRAE GAME: ATTACK³

$\text{Enc}_K(N, A, M)$

- 1: $r \xleftarrow{\mathcal{S}} \mathcal{R}$
- 2: $C \leftarrow \text{MEMEnc}_K(N, A, M; r)$
- 3: $\mathcal{S} \leftarrow \mathcal{S} \cup \{(N, A, C)\}$
- 4: **if** $b = 1$
- 5: $C \xleftarrow{\mathcal{S}} \{0, 1\}^{|M|+|\tau_1|+|\tau_2|+|r|}$
- 6: $d \leftarrow 1$
- 7: **return** C

$\text{Dec}_K(N, A, C)$

- 1: **if** $b = 1 \vee (N, A, C) \in \mathcal{S}_{\text{flt}} \cup \mathcal{S}$
- 2: **return** \perp
- 3: **else**
- 4: **return** $\text{MEMDec}_K(N, A, C)$

Fault Resilient Random Oracle

RANDOM ORACLES

- A random oracle in this work refers to an arbitrary input length and fixed output length random function.
- Unlike a PRF, a random oracle has no meaning implementation that can be faulted.
- We could view the random oracle as a large table that can be faulted, but that is not very useful.

KEY OBSERVATIONS

1. A hash function that is collision-resistant remains collision-resistant with faults. (Maybe not so obvious)
2. Preimage resistance is less clear: the adversary can force a faulty hash value that corresponds to a given input message.
3. Random salts prevent this type of attacks.
4. Randomness needs to be synchronized during verification.
5. Can we do something even stronger?

RANDOM ORACLES VS. HASH FUNCTION

- Usually we are using the random oracle model to argue about the security of a hash-based scheme.
- It is more meaningful to argue about the security of the random oracle in the relation to the implementation of the actual hash function.

- Use the hash function implementation to find out the effect of the fault.
- Use the random oracle to generate the tag.
- We need random salt to prevent certain prefix attacks.

The frRO Oracle

INIT

- 1: for $y \in \{0, 1\}^*$
- 2: $\text{RO}(y) \stackrel{\$}{\leftarrow} \perp$
- 3: $\mathcal{R}_{\text{flt}} \leftarrow \emptyset$

 $\text{frRO}^f(x; r, \mathcal{F})$

- 1: if $r \in \mathcal{R}_{\text{flt}}$ then bad
- 2: $\mathcal{R}_{\text{flt}} \leftarrow \mathcal{R}_{\text{flt}} \cup \{r\}$
- 3: if $\mathcal{F}.mod = \text{nof}$
- 4: if $\text{RO}(r||x) = \perp$
- 5: $\text{RO}(r||x) \stackrel{\$}{\leftarrow} \{0, 1\}^{|h|}$
- 6: $Z \leftarrow \text{RO}(r||x)$
- 7: else if $\mathcal{F}.v = r$
- 8: $r \leftarrow r \oplus \Delta$
- 9: if $r \in \mathcal{R}_{\text{flt}}$ then bad
- 10: $\mathcal{R}_{\text{flt}} \leftarrow \mathcal{R}_{\text{flt}} \cup \{r\}$
- 11: if $\text{RO}(r||x) = \perp$
- 12: $\text{RO}(r||x) \stackrel{\$}{\leftarrow} \{0, 1\}^{|h|}$

- 13: $Z \leftarrow \text{RO}(r||x)$
- 14: else if $\mathcal{F}.v = x$
- 15: $x \leftarrow x \oplus \Delta$
- 16: if $\text{RO}(r||x) = \perp$
- 17: $\text{RO}(r||x) \stackrel{\$}{\leftarrow} \{0, 1\}^{|h|}$
- 18: $Z \leftarrow \text{RO}(r||x)$
- 19: else
- 20: if $\text{RO}(r||x) = \perp$
- 21: $\text{RO}(r||x) \stackrel{\$}{\leftarrow} \{0, 1\}^{|h|}$
- 22: $(M^f, h) \leftarrow \text{Fault}(H(r||x), \mathcal{F})$
- 23: $\Delta \leftarrow H(r||x) \oplus h$
- 24: $Z \leftarrow \text{RO}(r||x) \oplus \Delta$
- 25: return (r, M^f, Z)

 $\text{frRO}(x; r)$

- 1: $\mathcal{F}.mod \leftarrow \text{nof}$
- 2: return $\text{frRO}^f(x; r, \mathcal{F})$

CLAIMS ON THE FRRO

Theorem

As long as the bad event is never set, then frRO is indistinguishable from a fault-free random oracle.

Conjecture

If a hash function H is indifferentiable from a random oracle, then its faulty implementation with differential faults is indifferentiable from an frRO.