DoveMAC

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Section 1

Motivation

Message Authentication Codes



- Goal: Data authentication via unforgeable authentication tags
- Stateful, randomized, nonce-based, or stateless deterministic (our focus)

Message Authentication Codes MAC and PRF Security



$$\mathbf{Adv}_{F}^{\mathsf{MAC}}(\mathbf{A}) \stackrel{\mathsf{def}}{=} \Pr_{K \twoheadleftarrow - \mathcal{K}} \left[\mathbf{A} \text{ forges} \right]$$

Message Authentication Codes MAC and PRF Security



 $\Delta_{\mathbf{A}}(X;Y) := \left| \Pr\left[\mathbf{A}^X \Rightarrow 1 \right] - \Pr\left[\mathbf{A}^Y \Rightarrow 1 \right] \right| \text{ over random choice of keys, oracles } X \text{ and } Y, \text{ and coins of } \mathbf{A} \text{ if any.}$ \$ returns $|F_K(M)|$ uniform random bits on any input M.

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DoveMAC

Block-cipher-based MACs



Tweakable Block Ciphers (TBCs) for MACs

TBCs [LRW02]:

Keyed families of permutations

 $\widetilde{E}: \mathbb{F}_2^k \times \mathbb{F}_2^t \times \mathbb{F}_2^n \to \mathbb{F}_2^n$

 \blacksquare Additional public tweak T

(Not only) For MACs, tweaks are useful for:

- \blacksquare Domain separation \implies security
- $\blacksquare \ \mbox{Additional message input} \implies \mbox{efficiency}$

Constructions:

- PMAC_TBC1k/PMAC_TBC3k [Nai15]
- HaT [CLS17]
- ZMAC [IMPS17]
- Hashes in TBC-based AE schemes



TBC-based Parallel MACs: ZMAC [IMPS17]

Combines:

- + High security: (n+t)/2 bits
- + Parallelizable
- + High efficiency: n + t bits per primitive call

But:

- Needs relatively much memory
- May be a obstracle for microcontrollers or constrained environments



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 $\begin{array}{c} M_{L}^{L} & M_{R}^{R} & M_{L}^{L} & M_{R}^{R} \\ \hline \\ L \rightarrow \bigoplus \\ \overline{E}_{K} & \downarrow \\ \hline \\ \overline{E}_{K} & \downarrow \\ \hline \\ \hline \\ Y_{0} & \downarrow \\ Y_{0} & \downarrow \\ \hline \\ \end{array}$

ZMAC [IMPS17]

Can we keep the high rate and high security of ZMAC but reduce its state size?

Section 2

DoveMAC

DoveMAC Hash



- Processes (n + t)-bit/TBC call
- \blacksquare Top: t bits, extended or truncated after each call
- Bottom: *n* bits
- TBC output feed-forward to bottom lane after each call
- Checksum $\Theta = \sum_{i=1}^{m} T_i$ needed for beyond-birthday security

DoveMAC Finalization



- Instance of Hash-as-Tweak (HaT) [CLS17] or its generalization Hash-then-TBC (HtTBC) [LN17]
- Easily extendable to variable-output-length PRF
- \blacksquare *n*-bit-secure if hash function *H* optimal
- Single-key version easily obtainable: reserve one tweak domain bit

Section 3

Proof Sketch

Proof Sketch: PRF Security of DoveMAC

Steps:

- Replace primitives with ideal tweakable permutations
- **2** Reduce to Hash-then-TBC
- 3 Upper bound collision probability of DoveHash
- Upper bound truncated-almost universality of DoveHash



Proof Sketch: Notions

Definition 1 (Collision Probability)

Collision among at most q pairwise distinct messages $M\neq M'$ of at most m b-bit blocks each and σ b-bit blocks in total:

$$\operatorname{coll}_{H}(b,q,m,\sigma) \stackrel{\text{def}}{=} \Pr_{\substack{K \leftarrow \mathcal{K} \\ M \neq M'}} \left[H_{K}(M) = H_{K}(M') \right] \,.$$



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Definition 2 (Truncated Almost-Universality)

 $H: \mathcal{K} \times \mathcal{M} \to \mathbb{F}_2^t \times \mathbb{F}_2^n$ is (t, n, ϵ) -truncated-AU if for all $M \neq M'$:

$$\sum_{\Delta \in \mathbb{F}_2^n} \Pr_{K \leftarrow \mathcal{K}} \left[H_K(M) \oplus H_K(M') = (0^t, \Delta) \right] \le \epsilon.$$



Proof Sketch: (1) Ideal Primitive

■ Replace primitives with ideal tweakable permutations: From \widetilde{E}_{K_1} , \widetilde{E}_{K_2} from $K_1, K_2 \leftarrow \mathcal{K}$ $\implies \widetilde{\pi}, \widetilde{\pi}' \leftarrow \widetilde{\mathsf{Perm}}(\mathbb{F}_2^t, \mathbb{F}_2^n)$



$$\mathbf{Adv}_{\mathsf{DoveMAC}\left[\widetilde{E}_{K_{1}},\widetilde{E}_{K_{2}}\right]}^{\mathsf{PRF}}(\mathbf{A}) \leq \mathbf{Adv}_{\mathsf{DoveMAC}\left[\widetilde{\pi},\widetilde{\pi}'\right]}^{\mathsf{PRF}}(\mathbf{A}') + (\sigma + q) \cdot \mathbf{Adv}_{\widetilde{E}_{K}}^{\mathsf{TPRP}}(\mathbf{A}'') \,.$$

Proof Sketch: (2) Reduce to HtTBC

$$\mathbf{Adv}_{\mathsf{DoveMAC}\left[\widetilde{\boldsymbol{\pi}},\widetilde{\boldsymbol{\pi}'}\right]}^{\mathsf{PRF}}(\mathbf{A}) \leq \mathbf{Adv}_{\mathsf{HtTBC}\left[\widetilde{\boldsymbol{\pi}'},\mathsf{DoveHash}\left[\widetilde{\boldsymbol{\pi}}\right]\right]}^{\mathsf{PRF}}(\mathbf{A})$$



Theorem 3 (PRF Security of HtTBC [LN17])

Let H denote DoveHash $[\widetilde{\pi}]$. Assume that

 $\operatorname{coll}_H(n+t,q,m,\sigma) \leq \epsilon_1,$

and H is (t, n, ϵ_2) -tAU. Let \mathbf{A} be a PRF adversary against HtTBC[$\tilde{\pi}', H$] that makes at most q queries consisting at most m (t + n)-bit blocks after padding each, that sum to at most σ (t + n)-bit blocks in total. Then

$$\mathbf{Adv}_{\mathsf{HtTBC}[\widetilde{\pi'},\mathsf{DoveHash}[\widetilde{\pi}]]}^{\mathsf{PRF}}(\mathbf{A}) \leq \epsilon_1 + \frac{\binom{q}{2} \cdot \epsilon_2}{2^n} \,.$$

Structure Graphs [BPR05]

- Vertices \mathcal{V} : State values $v_i = B_i = (U_i, S_i)$
- Edges \mathcal{E} : transitions $(v_i, v_{i+1}, \lambda_i)$
- Labels Λ : $\lambda_i = (T_i, I_i)$
- Walk: Sequence of vertices $\mathbf{v} = (v_0, \dots, v_m)$





Bad structure graphs in a message M:





$$m, \sigma < 2^{n-2}$$

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Good structure graphs of messages M and M':



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Lemma 4 (Collision Probability of DoveHash $[\tilde{\pi}]$)

Let $\sigma < 2^{n-2}$. Then,

$$\operatorname{coll}_{\operatorname{DoveHash}[\widetilde{\pi}]}\left(t+n,q,m,\sigma\right) \leq \frac{4\sigma}{2^n} + \frac{4qm^2 + 4q^2m^2}{2^{n+\min(n,t)}}\,.$$

Bad walks: output loop or non-trivial output collision



$$m, \sigma < 2^{n-2}$$

Bad walks: output loop or non-trivial output collision

Collision of $X_i = X_j$ in M:



$$\Pr[\mathsf{bad}_1] \le \frac{\binom{m}{2}}{2^n - 2m}$$
Collision $X_i = X'_j$ between M and M' :

$$\Pr[\mathsf{bad}_2] \le \frac{\binom{m}{2}}{2^n - 2m}$$

 $m, \sigma < 2^{n-2}$

Bad walks: output loop or non-trivial output collision

Collision of $X_i = X_i$ in M: $\Pr[\mathsf{bad}_1] \le \frac{\binom{m}{2}}{2^n - 2m}$ Collision $X_i = X'_i$ between M and M': $\Pr[\mathsf{bad}_2] \le \frac{\binom{m}{2}}{2n-2m}$ $\Pr[\mathsf{bad}] \le \mathsf{coll}_{\mathsf{DoveHash}[\widetilde{\pi}]} \left(t+n, 2, m, 2m\right) + 2 \cdot \frac{\binom{m}{2}}{2n-2\pi}$ $\leq \operatorname{coll}_{\operatorname{DoveHash}[\widetilde{\pi}]}(t+n,2,m,2m) + \frac{2m^2}{2m}.$

 $m,\sigma<2^{n-2}$

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 Y_0

Good walks: collision in X = X' without bad event



$$m, \sigma < 2^{n-2}$$

Good walks: collision in X = X' without bad event

$$\Delta \Theta \neq 0^{t}: \qquad \Pr[\mathsf{good}_{1}] \leq \frac{2^{n-\min(t,n)}}{2^{n}-2m}$$

$$\sum_{\substack{X_{0} \to \frac{c}{2} \\ Y_{0} \to 0}} \sum_{\substack{Y_{1} \to 0 \\ Y_{0} \to 0}} \sum_{\substack{Y_{1} \to 0 \\ Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0 \\ Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0 \\ Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0 \\ Y_{2} \to 0}} \sum_{\substack{Y_{2} \to 0}} \sum_{\substack$$

$$m, \sigma < 2^{n-2}$$

Good walks: collision in X = X' without bad event



 $m, \sigma < 2^{n-2}$



Lemma 5 (tAU Upper Bound of DoveHash $[\tilde{\pi}]$)

Let $m, \sigma < 2^{n-2}$. Then, $\mathsf{DoveHash}[\widetilde{\pi}]$ is (t, n, ϵ) -tAU for

$$\epsilon \leq \operatorname{coll}_{\operatorname{DoveHash}[\widetilde{\pi}]}\left(t+n,2,m,2m\right) + \frac{2m^2}{2^n} + \frac{4}{2^{\min(n,t)}}$$

Proof Sketch: Summary



Theorem 6 (PRF Security of DoveMAC)

Let $\tilde{\pi}, \tilde{\pi}' \leftarrow \widetilde{\operatorname{Perm}}(\mathcal{T}, \mathcal{B})$. Let A be a PRF adversary on DoveMAC $[\tilde{\pi}, \tilde{\pi}']$ s.t. A asks at most q queries that consist of at most $m < 2^{n-2}$ (t+n)-bit blocks after padding each, and that sum to at most $\sigma < 2^{n-2}$ (t+n)-bit blocks in total. Then

$$\mathbf{Adv}_{\mathsf{DoveMAC}[\widetilde{\pi},\widetilde{\pi'}]}^{\mathsf{PRF}}(\mathbf{A}) \leq \frac{4\sigma}{2^n} + \frac{q^2m^2}{2^{2n}} + \frac{2q^2 + 4qm^2 + 4q^2m^2}{2^{n+\min(n,t)}}$$

Section 4

Implementation

Implementation

	Message length (bytes)										
		ATmega 328p					RAM				
Scheme	64 128 2	$256\ 512$	1024	2048	4096	64	128	256	512	1024	(bytes)
DoveMAC[Skinny-64-128]	760 616	544 508	490	481	476	758	614	542	506	488	176
ZMAC1[Skinny-64-128]	$1013\ 757$	630 566	534	518	510	1009	755	627	564	532	236

Table: Rounded inverse throughputs in cycles/byte and RAM storage (bytes).

- Instantiation with Skinny-64-128 [BJK⁺16]
- Widespread 8-bit Atmel microcontrollers
- Comparison with ZMAC1 (ZHash [IMPS17] with HtTBC as finalization [Nai18])
- Base: Skinny AVR implementation by [BJK⁺16, rwe18] for both

Code available at https://github.com/medsec/dovemac

Section 5

Summary



- Sequential TBC-based MAC
- High rate: (n + t) bits/TBC call
- **High security:** $\min(n, (n+t)/2)$ bits without nonces
- Lower state size than ZMAC
- Easily extendable to variable-output-length PRF with Hash-then-TBC
- 2 keys, but single-key version easily obtainable by using tweak bit as domain

Limitations

- Has grown complex
- Simpler and smaller high-rate schemes (as part of AE schemes) appeared since
- But: Nonce essential for high security





Questions?

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