Kevin Atighehchi 1 and Alexis $\mathsf{Bonnecaze}^2$

¹Université de Caen Normandie, ENSICAEN, CNRS, GREYC, France

²Aix Marseille Université, Centrale Marseille, CNRS, I2M, France

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- An overview of tree hash modes
- Directions
- 2-level trees
- Balanced k-ary tree Asymptotic efficiency
- New topologies for new complexity results
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- A tree mode for live streaming
- Concluding remarks

Summary

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Objectives

Choosing a tree topology is motivated by several possible objectives:

- Minimizing the parallel time (whether or not the number of processors is bounded)
- Minimizing the number of processors for an optimal parallel time
- Optimizing the memory consumption

Objectives of this work:

- Give an overview
- Propose tree topologies with the aim of:
 - Minimizing the parallel time and the number of processors for a constrained memory usage
 - Minimizing the number of processors and the memory usage for an (asymptotically) optimal parallel time

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Assumptions and notations

The *outer* hash function is constructed on top of an *inner* function f according to a tree mode.

Assumptions :

- PRAM model with EREW strategy
- The operated inner function is a serial hash function
- Processing a k-ary node costs $\Theta(k)$ units of time
- For simplicity, all the leaves are at the same depth
- To give substance to the constructed outer hash function, we assume the use of SAKURA coding



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Memory usage of a sequential execution

We refer to the *auxiliary space* required for executing the hash function, *i.e.* not counting the message.

Memory usage = number of hash states in memory

- For a serial hash function, it is one hash state
- For a tree-based hash function, ${\cal O}(h)$ hash states where h is the tree height
 - Compute the *highest node first*
 - Strategy used in Merkle tree traversal
 - For nodes of high arity, use a threshold value d to trigger the consumption of $\ensuremath{\mathsf{CVs}}$

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Parallel time

Further notations:

- t(m) denotes the sequential running time to process m chaining values or message blocks
- $T({\bf R})$ denotes the parallel time to process a node ${\bf R}$ and all its descendants

Let us consider a node, denoted R_* , and its children, denoted R_i for $i = 1 \dots k_{R_*}$, indexed in the order in which their corresponding chaining values are processed in R_* . We have

$$T(\mathbf{R}_*) \approx \begin{cases} t(k_{R^*}) & \text{if } \mathbf{R}^* \text{ is a base} \\ & \text{level node,} \\ \max_{i=1...k_{R_*}} \{T(\mathbf{R}_i) + t(k_{R_*} - i + 1)\} & \text{otherwise.} \end{cases}$$

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About the choice of a tree mode, two camps:

- 1 Think about general-purpose and low-end computers
- 2 Think about the potential speedup

An overview of tree hash modes

One invariant in the discussions: a single tree mode is desired to fit all the computing platforms.

2-level trees

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Choice between scalability and reduced root node arity:

• Scalability (e.g. SP 800-185 ParallelHash; KangarooTwelve)



• Something dedicated to a fixed number q of processors, e.g. q=2



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Balanced k-ary tree

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Classic balanced k-ary hash tree

• w/o height restriction (k = 2)



• with height restriction ($k=2\text{, }h\leq3\text{)}$



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Asymptotic efficiency (in Big-O)

Asymptotic efficiency using Big-O notation of existing tree hash modes:

Mode	Live	Memory (sequential)	Number of	Parallel running time	Comments
	8	(P	(ideal case)	
1	√	1	n	n	root of unlimited arity
2S	-	1	a	n/a	not scalable (but
2L	\checkmark	q	Ч	10/9	reduced amount of work)
3	\checkmark	$\log n$	n	$\log n$	tree of unlimited height

where n is the number of blocks of the message and q is a fixed number of processors. (q not hidden in the Big-O)

Criterias:

- Memory consumption of a sequential execution
- Number of processors to reach the lowest/ideal parallel running time
- The parallel time referring to the lowest running time achievable with the aforementioned number of proc.

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New topologies for new complexity results

New plausible modes for streaming stored content (4S, 5S and 6S) and their variants for streaming live content (4L, 5L, 6L). Their efficiencies are expressed in Big-O.

Tree	Live	Memory usage	Number	Parallel	Priority
mode	streaming	(sequential)	of	running	
			processors	time (ideal	
				case)	
1	\checkmark	1	n	n	memory consumption
2S	-	1	a	n/a	memory and total
2L	\checkmark	q		n/q	work (for a fixed q)
3	\checkmark	$\log n$	n	$\log n$	parallel time
4S	-	1	$m^{1-\epsilon}$	m^{ϵ}	memory consumption
4L	\checkmark	1	11	11	memory consumption
5S	-	_	_	_	none (any trade off)
5L	\checkmark	_	_	_	none (any trade-on)
6S	-	$\log n = \log \log n$	n	logn	parallel time
6L	\checkmark	$\log n - \log \log n$	$\log n$	log n	paraner time

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A tree mode for live streaming (Mode 4L)

Processing of 10 blocks with a 2-level tree



Observations:

- Increasing node arities at level 1
- Number k of CVs is $\arg\min_k\sum_{i=1}^k i\geq n$, i.e. $k=O(n^{1/2})$
- Memory consumption of a serial implementation: ${\cal O}(1)$

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Generalizing for a constant tree height

The underlying tree can be defined recursively until a given tree height h is reached:

- Initially (before tree pruning), the root node is assumed to be of arity ∞
- Let k the arity of a descendant node, its i-th child is of arity i+1, for $1\leq i\leq k$
- The message blocks being at depth k, prune the unneeded branches

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Generalizing for a constant tree height



Theorem

Given $\epsilon = 1/h$ where h is the height, Mode 4L has an ideal parallel running time in $O(n^{\epsilon})$ by using only $O(n^{1-\epsilon})$ processors, and requires $O(1/\epsilon) = O(1)$ hash states in memory to process the message with a single processor.

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Generalizing for a constant tree height (Ctd)

Let n the number of message blocks.

Steps to determine the parallel time:

- Once pruned, let x denote the root node arity, a_{br} the arity of the righmost node at the base level, and h the height of the tree
- By construction of the tree, $a_{br} < x + h$
- Let T be the parallel time to compute the hash. $T=O(a_{br}+h)=O(a_{br})=O(x) \mbox{ since } h \mbox{ constant}$
- Let $n_{max}(x)$ be the maximum number of blocks covered by the tree when pruning only the edges at the root level. We show that

$$n_{max}(x) \sim \frac{x^h}{h!}$$
 and $n_{max}(x) \sim n$.

As a result, $x = O(n^{1/h})$

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Generalizing for a constant tree height (Ctd)

Let \boldsymbol{w} be the number of involved processors.

Remaining steps to determine w:

• Let $w_{max}(x)$ be the number of nodes at level 2 when pruning only the edges at the root level. We have

$$w_{max}(x) \sim \frac{x^{h-1}}{(h-1)!}$$

• Because $w_{max}(c \cdot x) = O(w_{max}(x))$ and $x = O(n^{1/h})$, it follows that

$$w = O(n^{\frac{h-1}{h}})$$

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Pending issues:

- Asymptotic complexities in *O*?! Why not exact complexities?
 - They depend on the inner function parameters
 - Sometimes, only numerical results can be expected
 - Analytical or numerical result \neq measurement in practice
- To what extent these topologies can be used? *E.g.*
 - Evaluation of software implementations
 - Hybrid Multithreaded/SIMD implementations
- The question of a parallelizable output extension?

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Thanks for your attention...

Questions ?